

Pigeon Point[®] BMR-AVR-AMCm Reference Design Board Management Reference Design for AdvancedMC[™] Modules

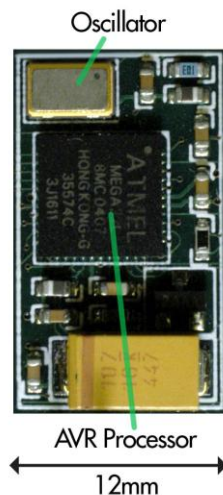
The BMR-AVR-AMCm design is one of a series of Pigeon Point Systems (PPS) Board Management Reference designs. This member of the series provides a Module Management Controller (MMC) for Advanced Mezzanine Cards (AdvancedMCs or AMC)s and is based on an AVR[®] micro controller from Atmel.

This reference design is delivered in a Pigeon Point Board Management Starter Kit (which is detailed in a separate Product Brief). The kit includes:

- Schematics for a complete MMC subsystem, ready for integration into the design of your AMC, with adaptation as necessary.
- Firmware for that subsystem, delivered in source form and with development tools—ready for simple and quick adaptation to the specific requirements of your product.
- One-stop support for hardware, firmware and software used in developing and delivering your Pigeon Point BMR-based MMC.

The photo below shows the core of a BMR-AVR-AMCm MMC. The active components are:

- An Atmel ATmega64/128 highly integrated micro controller.
- An optional external oscillator to provide the operating frequency.



Specification compliant and interoperability tested

- AMC.0 R2.0, the AdvancedMC base specification, which covers AdvancedTCA carriers
- MTCA.0 R1.0, the μ TCA base specification, which covers μ TCA carriers
- HPM.1, the PICMG Firmware Upgrade specification
- IPMI v1.5, document revision 1.1, plus relevant errata
- Thoroughly tested with other ATCA and μ TCA management components at PICMG (μ TCA-IWs (Interoperability Workshops)

Full support for core hardware requirements

- AMC hot swap interfaces (handle and blue LED)
- Geographic address detection from carrier
- Control of E-Keying governed interfaces (for both fabric and clock E-Keying)
- Optional persistence of above controls across MMC resets
- Managed by on-carrier IPMB-L
- UART-based payload and serial debug interfaces
- Thermal sensors (LM60 analog and/or DS75 digital)
- FRU LED management

Small footprint and low power

- Core MMC can fit in 24mm x 12mm footprint
- Active components consume only 30mA of max power

Comprehensive, readily adaptable firmware

- All mandatory and many optional IPMI/AMC commands supported over IPMB-L
- Numerous PPS extension commands, primarily used over the payload and debug serial interfaces
- Serial interface protocol based on IPMI Terminal Mode
- Payload alert notifications over payload interface for sensor events and receipt of reset/shutdown commands
- PICMG HPM.1 firmware upgrade support
- Continued support for legacy Pigeon Point firmware upgrades facilities
- Simple—but highly flexible—configuration of firmware features

ASCII-based serial interface protocol supported via UARTs to payload processor and serial debug interface

- Based on IPMI-defined Terminal Mode of the Serial/Modem Interface (and referenced as Serial Interface Protocol Lite—SIPL)
- Same protocol used for both serial interfaces
- Uses ASCII-encoded raw IPMI messages, which are handled by the IPM Controller essentially like IPMB messages
- PPS extension commands implemented as IPMI-compliant OEM messages

Sophisticated, HPM.1-compliant support for firmware upgrades

- Firmware upgrades over any IPMI interface to the IPM Controller, with redundant copies and automatic fallback after failed upgrade
- IPM Controller is fully functional during upgrade
- Bootloader can be upgraded without using JTAG
- Open source `ipmitool` supplied as upgrade agent
- HPM.1 compliance means that any compliant upgrade agent can upgrade any compliant IPM Controller

Optional support for persistent modifications to Sensor Data Records

- Non-volatile copy of SDR Repository can be configured in on-board EEPROM
- Sensor threshold and hysteresis values can be configured dynamically via PPS extension commands, and are thereafter persistent across power cycles and resets of the board

Simple, but powerful, firmware configuration mechanisms

- Configuration variables in a single `config.h` source file parameterize and determine inclusion/exclusion of subsystems during firmware image build
- Binary configuration files for FRU Information and Sensor Data Records (SDR) merged into firmware image
- FRU Information and SDR files produced from textual representations by special supplied compilers

Comprehensive AVR development environment included

- Cross GNU C compiler, GNU GDB and binary utilities and optimized `libc` for AVR architecture
- JTAG-based debugging and firmware download
- Communication utility for Atmel JTAG ICE tool (the latter purchased separately)
- Supported under both Linux and Windows on x86 hosts

Key extensions beyond required IPMI/ATCA/AMC/HPM.1 commands and functionality

- Graceful Reboot and Issue Diagnostic Interrupt options in FRU Control command
- Send Message
- Get Sensor Reading Factors
- Get/Set Sensor Hysteresis
- Get/Set Sensor Thresholds
- Get/Set Sensor Event Enable
- Re-arm Sensor Events
- Get Sensor Event Status
- Abort Firmware Upgrade
- Initiate Upgrade Action
- Get Upgrade Status
- Query Self-test Results
- Query Rollback Status
- Initiate Manual Rollback

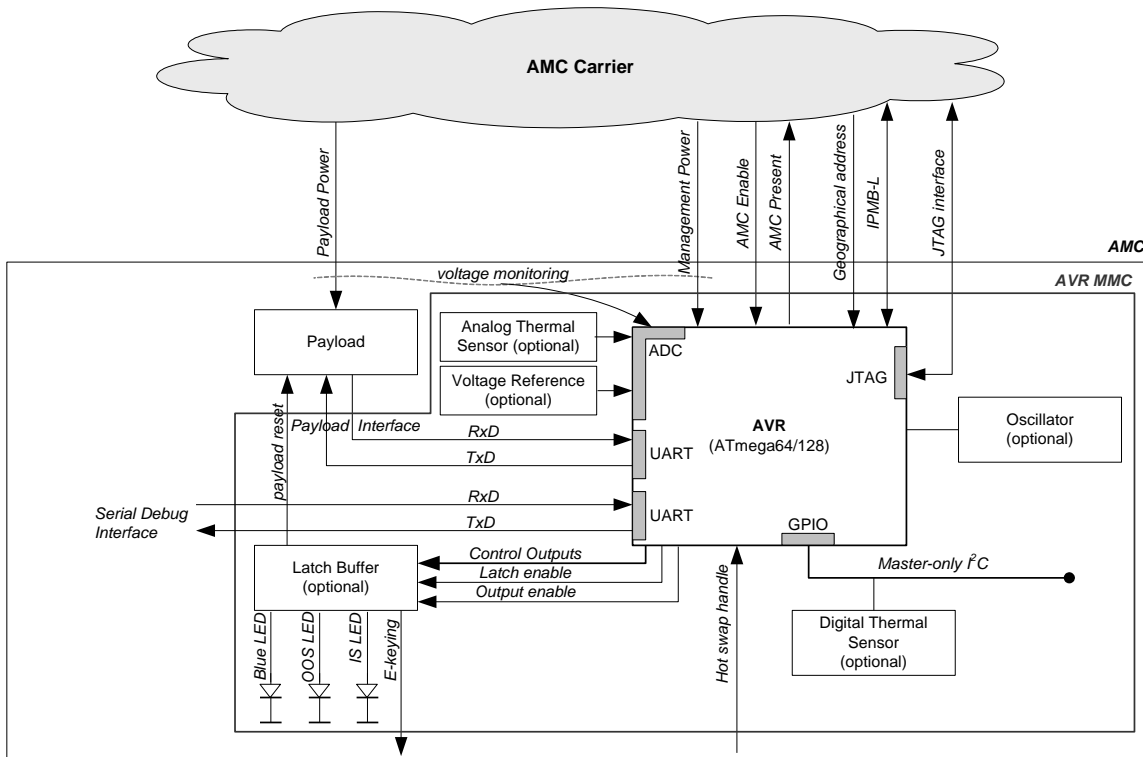
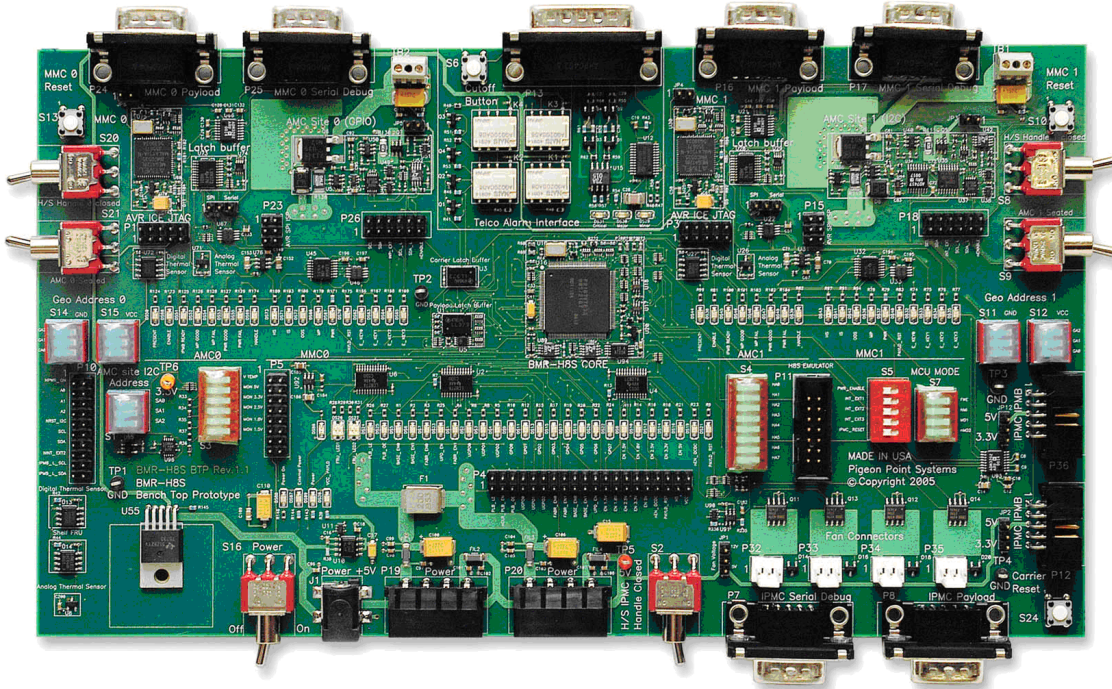
Rich set of PPS extension commands

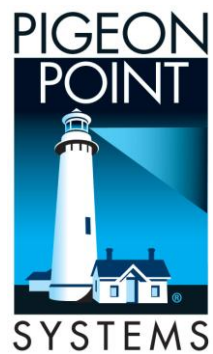
- Get Status
- Get/Set Serial Interface Properties
- Get/Set Debug Level
- Get/Set Handle Switch
- Get/Set Payload Communication Timeout
- Disable/Enable Payload Control
- Reset MMC
- Hang MMC¹
- Graceful Reset
- Diagnostic Interrupt Results
- Get/Set Payload Shutdown Timer
- Get/Set Geographic Address
- Get/Set Payload Shutdown Timeout
- Set Test Flags
- Set EEPROM SDR Data
- Set EEPROM SDR Hysteresis
- Set EEPROM SDR Thresholds
- Reset EEPROM SDR Repository
- Backend Power Control

¹ This function is used to test the MMC watchdog.

Reference Implementation

The bench top implementation of this reference design is the same H8S-based bench top used with the BMR-H8S-ATCA reference design. This board already implements two AMC sites and an MMC at each site, based on the BMR-AVR-AMCm reference design. In addition, the board includes the needed Carrier IPMC functionality supporting those sites. Thus, this board serves as a bench top implementation of both the BMR-H8S-AMCc (Carrier) and BMR-AVR-AMCm (Module) reference designs. In addition to the core facilities of those reference designs, the board includes example implementations of the optional controller features and numerous LEDs, switches and headers to allow lab experimentation with the behavior of the controllers, including pre-supported options and any desired firmware customizations. Below the photo is a block diagram of the AVR-based Module Management Controller.





For more information, visit our website at <http://www.pigeonpoint.com>

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